

Rear view; Pins facing upwards; All dimensions are in mm; Header pitch is 2.54 mm. Drawing not to scale.

Motor – sensor configurations							
Motor Sensor	PMSM	BLDC	DC BRUSH	STEP (2-ph)	STEP ² (3-ph)		
Incr. Encoder	T		T	(7)	①		
Incr. Encoder + Hall	T	T					
Analog Sin/Cos encoder	T	T	T	(7)	3		
SSI	T	T	T	T	T		
BiSS-C	T	T	T	(7)	3		
EnDAT ¹	T	T	T	T	3		
Linear Halls	T						
Tacho			T				
Open-loop (no sensor)				5	3		

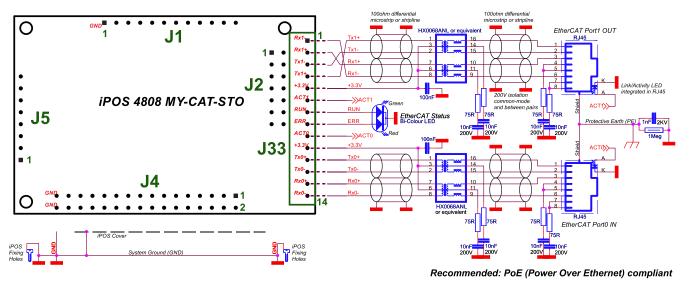
- ¹ Available starting with F515K firmware version
- ² Sensor used only for step loss detection

Features

- Motion controller and drive in a single compact unit based on MotionChip ™ technology
- Universal solution for control of rotary and linear brushless, brushed and 2 or 3-phase step motors
- Advanced motion control capabilities (PVT,S-curve, electronic cam)
- Motor supply: 11-50V; Logic SELV/ PELV supply: 9-36V; STO SELV/ PELV supply: 18-40V
- Output current: 8A cont. (BLDC mode); 20A_{PEAK}, up to 100kHz PWM

- Feedback Devices (dual-loop support)
 - 1st feedback devices supported:
 - Incremental encoder interface (single ended or differential)
 - Analogue sin/cos encoder interface (differential 1V_{pp})
 - Digital Hall sensor interface (single-ended and open collector)
 - Linear Hall sensors interface
 - pulse & direction interface (single ended) for external (master) digital reference
 - 2nd feedback devices supported:
 - Incremental encoder interface (differential)
 - pulse & direction interface (differential) for external (master) digital reference
 - ■BISS-C / SSI / EnDAT¹ encoder interface
- STO: 2 safe torque-off inputs, safety integrity level (SIL3/Cat3/PLe) acc. to EN61800-5-1;-2/ EN61508-3;-4/ EN ISO 13849-1.
- 6 digital inputs, 12-36V, PNP/NPN programmable: 2 for limit switches, 4 general-purpose
- 5 digital outputs, 5-36V, 0.5A, NPN open-collector: Ready, Error, 3 general-purpose
- 2 analogue inputs: 12-bit, 0-5V: Reference, Feedback or general purpose
- RS-232 serial & dual 100Mbps EtherCAT® interfaces
- EtherCAT® with CAN application protocol over EtherCAT (CoE), File over EtherCAT (FoE) and Ethernet over EtherCAT (EoE)
- 127 h/w addresses selectable by h/w pins configuration
- 16k x 16 SRAM memory for data acquisition
- 16k x16 E²ROM to store setup data, TML motion programs, cam tables and other user data
- Operating ambient temperature: 0-40°C (over 40°C with derating)
- NTC/PTC analogue Motor Temperature sensor input

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	Mating Connectors							
Ref	Producer	Part No.	Description					
J1	Samtec	SSQ-112-01-G-S	1x12 contacts, socket 2.54mm-pitch accepting 0.635mm square pin					
	FCI	87606-307LF	2x7 contacts, socket, 2.54mm-pitch					
J2	TE Connectivity	534206-7	accepting 0.635mm square pin					
J33	Samtec	SSQ-114-01-G-S	1x14 contacts, socket 2.54mm-pitch accepting 0.635mm square pin					
J4	Samtec	SSQ-116-01-G-D	2x12 contacts, socket 2.54mm-pitch accepting 0.635mm square pin					
J5	Samtec	SSQ-108-01-G-S	1x8 contacts, High-current socket 2.54mm-pitch accepting 0.635mm square pin					

	Pin	Name	Type	Description	
	1	GND	-	Return ground for all signals. Internally connected to J4 pins 31 and 32, to metallic cover, and to the 3 fixing screws	
	2	reserved	-	Reserved	
	3	Axis ID Bit6	ı		
	4	Axis ID Bit5	ı		
	5	Axis ID Bit4	ı	8 bit H/W Axis ID register.	
7	6 Axis ID Bit3		ı	Connect pin to GND to set bit to 1.	
	7	Axis ID Bit2	ı	Up to 127 H/W axis ID combinations.	
	8	Axis ID Bit1 I			
	9 Axis ID Bit0		ı		
	10	reserved	-	Reserved for interface extensions†	
	11	reserved	-	Reserved for interface extensions†	
	12	reserved	-	Reserved for interface extensions†	
	Pin	Name	Type	Description	
	1,2	A/A+	0	Phase A for 3-ph motors, A+ for 2-ph steppers, Motor+ for DC brush motors	
75	3,4	B/A-	0	Phase B for 3-ph motors, A- for 2-ph steppers, Motor- for DC brush motors	
	5,6	C/B+	0	Phase C for 3-ph motors, B+ for 2-ph steppers	
-	7,8	Cr/B-	0	Chopping resistor / Phase B- for 2-ph steppers	

	Pin	Name	Туре	Description			
	1	RX1-	I/O	Receive/Transmit negative, OUT port. Connect to magnetics PHY RX1.			
	2	RX1+	I/O	Receive/Transmit positive, OUT port. Connect to magnetics PHY RX1.			
	3	TX1-	I/O	Transmit/Receive negative, OUT port. Connect to magnetics PHY TX1.			
	4	TX1+	I/O	Transmit/Receive positive, OUT port. Connect to magnetics PHY TX1.			
	5	3.3V	0	Return for center tap of magnetics PHY TX1 and RX1. Internally connected to J33 pin 10.			
	6	ACT1	0	Anode of Link/Activity LED for port OUT.			
133	7	RUN	0	Anode of Run LED (EtherCAT status machine).			
3	8	ERR	0	Anode of Error LED (EtherCAT status machine).			
	9	ACT0	0	Anode of Link/Activity LED for port IN.			
	10	3.3V	0	Return for center tap of magnetics PHY TX0 and RX0. Internally connected to J33 pin 5.			
	11	TX0+	I/O	Transmit/Receive positive, IN port. Connect to magnetics PHY TX0.			
	12	TX0-	I/O	Transmit/Receive negative, IN port. Connect to magnetics PHY TX0.			
	13	RX0+	I/O	Receive/Transmit positive, IN port. Connect to magnetics PHY RX0.			
	14	RX0-	I/O	Receive/Transmit negative, IN port. Connect to magnetics PHY RX0.			

				J	,					
	Pin	Name	Туре	Descriptio	n					
	1	STO1+	ı	Safe Torque Off input 1, positive input (opto-isolated, 18÷40V)	Apply between both					
	2	STO2+	I	Safe Torque Off input 2, positive input(opto-isolated, 18÷40V)	STO1+, STO2+ and STO1-, STO2- 24V DC from SELV/ PELV					
	3	STO1-	- 1	Safe Torque Off input 1, negative return (opto-isolated, 0V)	power supply for motor PWM output					
	4	STO2-	I	Safe Torque Off input 2, negative return (opto-isolated, 0V)	operation					
	5	LH1	- 1	Linear Hall 1 input						
	6	LH2	ı	Linear Hall 2 input						
	7	IN4	ı	12-36V general-purpose digital PN	NP/NPN input					
2	8	IN5	ı	12-36V general-purpose digital PN	NP/NPN input					
7	9	OUT0	0	5-36V 0.5A, general-purpose digi collector/TTL pull-up	tal output, NPN open-					
	10	OUT3/ Ready	0	5-36V 0.5A, drive Ready output, a collector/TTL pull-up. Also drives to						
	11	OUT1	0	5-36V 0.5A, general-purpose digital output, NPN open-collector/TTL pull-up						
	12	OUT2/ Error	0	 5-36V 0.5A, drive Error output, active low, NPN collector/TTL pull-up. Also drives the red LED 						
	13	REF	ı	Analogue input, 12-bit, 0-5V. Read general purpose analogue input	ds analog reference, or					
	14	FDBK / LH3	I	Analogue input, 12-bit, 0-5V. Reads an analogue feedback (tacho), or general purpose/ or Linear Hall 3						

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	Pin	Name	Туре	Description
	1	INO	1	12-36V general-purpose digital PNP/NPN
	2	IN1	1	input 12-36V general-purpose digital PNP/NPN input
	3	IN2/LSP	I	12-36V digital PNP/NPN input. Positive limit switch input
	4	IN3/LSN	ı	12-36V digital PNP/NPN input. Negative limit switch input
	5	B2-/Dir-/CLK- /MA-	I/O	Incr. encoder2 B- diff. input, or Dir, or Clockfor SSI & EnDAT, or Master- for BiSS; has 120Ω resistor between pins 5 and 7
	6	B1-/Cos-	I	Incr. encoder1 B- diff. input, or analogue encoder Cos- diff. input
	7	B2+/Dir+/CLK+/ MA+	I/O	Incr. encoder2 B+ diff. input, or Dir+-, or Clock+ for SSI & EnDAT, or Master+ for BiSS; has 120Ω resistor between pins 5 and 7
	8	B1+/Cos+	ı	Incr. encoder1 B single-ended, or B+ diff. input, or analogue encoder Cos+ diff. input
	9	A2+/Pulse+/ Data+/SL+	ı	Incr. encoder2 A+ diff. input, or Pulse+, or Data+ for SSI & EnDAT, or Slave+ for BiSS; has 120Ω resistor between pins 9 and 11
	10	A1+/Sin+	ı	Incr. encoder1 A single-ended, or A+ diff. input, or analogue encoder Sin+ diff. input
	11	A2- /Pulse-/ Data-/SL-	ı	Incr. encoder2 A- diff. input, or Pulse-, or Data-for SSI & EnDAT, or Slave- for BiSS; has 120Ω resistor between pins 9 and 11
	12	A1-/Sin-	I	Incr. encoder1 A- diff. input, or analogue encoder Sin- diff. input
4	13	Z2+	ı	Incr. encoder2 Z+ diff. input ; has 120Ω resistor between pins 13 and 15
	14	Z1+	ı	Incr. encoder1 Z single-ended, or Z+ diff. input,
	15	Z2-	- 1	Incr. encoder2 Z- diff. input; has 120Ω resistor between pins 13 and 15
	16	Z1-	I	Incr. encoder1 Z- diff. input
	17	Hall 1	I	Digital input Hall 1 sensor
	18	Reserved	-	Reserved. Do not connect.
	19	Hall 2	l l	Digital input Hall 2 sensor
	20	Reserved	<u> </u>	Reserved. Do not connect.
	21	Hall 3	ı	Digital input Hall 3 sensor
1	22	232TX	0	RS-232 Data Transmission
	23	+5V _{OUT}	0	5V output supply for I/O usage
	25	232RX Temp Mot	ı	RS-232 Data Reception NTC/PTC input. Used to read an analog
	26	Reserved		Reserved. Do not connect.
	27	+V _{LOG}	ı	Positive terminal of the logic supply input: 9 to 36V _{DC} from SELV/ PELV type power supply.
	28	OUT4	0	5-36V 0.5A, general-purpose digital output, NPN open-collector/TTL pull-up
	29	+V _{MOT}	ı	Positive terminal of the motor supply: 11 to $48V_{DC}$.
•	30	+V _{MOT}	ı	Positive terminal of the motor supply: 11 to $48V_{DC}$.
	31	GND	-	Return ground for all signals. Internally
	32	GND	_	connected to J4 pin 32, to J1 pin 1, to metallic cover, and to the 3 fixing screws

 $[\]ensuremath{^{\dagger}}$ leave unconnected if interface extensions are not used

Electrical characteristics

All parameters measured under the following conditions (unless otherwise specified):

- VLOG = 24 VDC; VMOT = 48VDC
- Supplies start-up / shutdown sequence: -any-Load current (sinusoidal amplitude / continuous BLDC, DC, stepper) = 8A

Load current (sinusoidal amplitude / continuous BLDC, DC, stepper) = 8A								
Operating Condit		Min.	Тур.	Max.	Units			
Ambient temperature			0		40¹	°C		
Ambient humidity		Non-condensing	0		90	%Rh		
Altitude / pressure	Altitude (vs. sea level)	-0.1 0 ²	0 ÷ 2.5	2	Km			
·	· Ambient Pressure			0.75 ÷ 1	10.0	atm		
Storage Conditions			Min.	Тур.	Max.	Units		
Ambient temperatu	ıre		-40		100	°C		
Ambient humidity		Non-condensing	0		100	%Rh		
Ambient Pressure			0		10.0	atm		
ESD capability		Not powered; applies to			±0.5	kV		
(Human body mod	el)	any accessible part Original packaging			±15	kV		
Mechanical Mounting			Min.	Тур.	Max.	Units		
Airflow	l			al convecti				
	Bet	ween adjacent drives	30		, 0.000	mm		
Spacing required for vertical	Bet	ween drives and nearby	30			mm		
mounting	wal							
-		ween drives and roof-top	20			mm		
		ween adjacent drives ween drives and nearby	4			mm		
Spacing required	wal	ls	5	<u> </u>		mm		
for horizontal mounting		ice needed for drive	10			mm		
		oval	-					
Insertion force		ween drives and roof-top ng recommended mating	15	TBD	TBD	mm N		
Extraction force		nectors	TBD	TBD	100	N		
Environmental Ch	narac	teristics	Min.	Тур.	Max.	Units		
Size (Length x	CI-	h - l - i	64	x 43.6 x 2	22	mm		
Width x Height)	GIO	bal size	~2.52 x 1.72 x 0.87		0.87	inch		
Weight				45		g		
Cleaning agents	Cleaning agents Dry cleaning is recommended			Water- or	Alcohol- b	ased		
5		ording to IEC60529,		IDOO				
Protection degree	UL			IP20		-		
Logic Supply Inpu	ut (+\	/ _{LOG})	Min.	Тур.	Max.	Units		
		ninal values	9		36	V _{DC}		
		Absolute maximum values, drive operating but outside			40	V _{DC}		
		ranteed parameters	8		40	V DC		
Supply voltage		solute maximum values,	-0.6		42	V _{DC}		
		tinuous	-0.0		42	VDC		
		olute maximum values,	-1		+45	V		
		ge (duration ≤ 10ms)		400		-		
Supply current		og = 12V og = 24V		130 90	280	mA		
очрріу сипепі	+V	og – 24V og = 40V		85	200	''' [\]		
Motor Supply Inp			Min.	Тур.	Max.	Units		
4 C. 1 - 1 - 1 - 1 - 1		ninal values	11	74.	50	V _{DC}		
	Abs	olute maximum values,				1		
	driv	e operating but outside	9		52	V_{DC}		
Supply voltage		ranteed parameters olute maximum values,				-		
,		tinuous	-0.6		54	V_{DC}		
		olute maximum values,	-1		F7	.,		
				1	57	V		
	sur	ge (duration ≤ 10ms) ^t	'					
	Idle	,		1	5	mA		
	Idle	erating	-20	1 ±8	5 +20	mA A		
Supply current	Ope Abs	erating solute maximum value,			+20	Α		
Supply current	Ope Abs	erating solute maximum value, rt-circuit condition						
,	Ope Abs sho (du	erating solute maximum value,	-20	±8	+20 26	A		
Supply Output (+	Idle Ope Abs sho (du	erating solute maximum value, rt-circuit condition ration ≤ 10ms) [†]	-20 Min .	±8	+20 26 Max.	A A Units		
Supply Output (+: Output voltage	Idle Ope Abs sho (du	erating solute maximum value, rt-circuit condition	-20 Min. 4.8	±8 Typ. 5	+20 26	A A Units		
Supply Output (+	Idle Ope Abs sho (du	erating solute maximum value, rt-circuit condition ration ≤ 10ms) [†]	-20 Min .	±8	+20 26 Max. 5.2	A A Units		
Supply Output (+: Output voltage Output current	Idle Ope Abs sho (du	erating solute maximum value, rt-circuit condition ration ≤ 10ms) [†]	-20 Min. 4.8	±8 Typ. 5 250	+20 26 Max. 5.2	A A Units		

¹Operating temperature at higher temperatures is possible with reduced current and power ratings ² iPOS4808 can be operated in vacuum (no altitude restriction), but at altitudes over 2,500m, current and power rating are reduced due to thermal dissipation efficiency.

 $^{^3}$ In case of forced cooling (conduction or ventilation) the spacing requirements may drop substantially down to zero as long as the ambient temperature is kept below the maximum operating limit

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Motor Outputs (A	Min.	Тур.	Max.	Units		
Nominal output	for DC brushed, st and BLDC motors based trapezoidal	with Hall- control			8	
current, continuous ¹	for PMSM motors sinusoidal control amplitude value)	(sinusoidal			8	Α
		for PMSM motors with FOC sinusoidal control (sinusoidal effective value)			5.66	
Motor output current, peak	maximum 2.5s		-20		+20	Α
Short-circuit protection threshold			±22	±26	±30	Α
Short-circuit protection delay			5	10		μS
On-state voltage drop	Nominal output cui including typical m connector contact	ating		±0.3	±0.5	٧
Off-state leakage current				±0.5	±1	mA
	Decemberded	F _{PWM}				
	Recommended value, for current ripple max. ±5% of	20 kHz	330			μН
		40 kHz	150			
	full range;	60 KHZ	120			
Motor inductance	+V _{MOT} = 36 V	80 kHz	80			
(phase-to-phase)		100 kHz	60			
(Minimum value, limited by short- circuit	20 kHz	120			μН
		60 kHz	40			
		40 kHz	30			
	protection; +V _{MOT} = 36 V	80 kHz 100 kHz	15 8			
		20 kHz	250			
Makan alaaksia al	Recommended	40 kHz	125			
Motor electrical time-constant	value for ±5% current	60 kHz	100			μs
(L/R)	measure-ment	80 kHz	63			μο
(=, -, -,	error	100 kHz	50			
Current measurement	FS = Full Scale ac			±4	±8	%FS
Digital Inputs (IN0, IN1, IN2/LSP	, IN3/LSN, IN4, IN5) ²	Min.	Тур.	Max.	Units
Mode compliance				PN	ΝP	
Default state	Input floating (wirir disconnected)	ng			LOW	
	Logic "LOW"		-10	0	2.2	
	Logic "HIGH"		6.3	24	36	
	Hysteresis		1.2	2.4	2.8	
Input voltage	Floating voltage (n connected)			0		٧
	Absolute maximun continuous	,	-10		+39	
	Absolute maximun (duration ≤ 1s)	n, surge	-20		+40	
Input current	Logic "LOW"; pulle	ed to GND		0	10	mA
	Logic HIGH			0	10	l

Mode compliance			N	PN		
Default state	Input floating (wiring disconnected)		Logic	HIGH		
	Logic "LOW"		0	2.2		
	Logic "HIGH"	6.3	24	36		
	Hysterezis	1.2	2.4	2.8		
Input voltage	Floating voltage (not connected)		15		V	
	Absolute maximum, continuous	-10		+39		
	Absolute maximum, surge (duration ≤ 1s) [†]	-20		+40		
	Logic "LOW"; Pulled to GND		8	10		
Input current	Logic "HIGH"; Pulled to +24V	0	0	0	mA	
Input frequency		0		10	kHz	
Minimum pulse		6			μs	
ESD protection	Human body model	±5			kV	

Digital Outputs (OUT0, OUT1, OU OUT4)	T2/Error, OUT	3/ Ready,	Min.	Тур.	Max.	Units	
Mode compliance	All outputs (OUT0, OUT1, OUT2/Error, OUT3/Ready)		NPN 24V				
	Not supplied (+V _{LOG} floating or to GND) High-Z (floating)						
	Immediately after power-	OUT0, OUT1, OUT4		Logic "HIGH"			
Default state	up	OUT2/Error, OUT3/ Ready		Logic "LOW"			
	Normal OUT0, OUT1, Logic "HIC		HIGH"				
	operation	OUT3/Ready		Logic	"LOW"		
	Logic "LOW"; (= 0.5A	output current			0.8	3	
	Logic "HIGH"; output	OUT2/Error, OUT3/ Ready	2.9	3	3.3		
Output voltage	current = 0, no load	OUT0, OUT1, OUT4	4	4.5	5	V	
	Logic "HIGH", to +V _{LOG}	external load		V_{LOG}			
	Absolute maxi continuous		-0.5		V _{LOG} +0.5		
	Absolute maxi (duration ≤ 1s)	t ,	-1		V _{LOG} +1		
	Logic "LOW",				0.5	Α	
	Logic "LOW", s pulse ≤ 5 sec.				1	Α	
Output current	Logic "HIGH", source current, external load to GND; V _{OUT} >= 2.0V				2	mA	
		OUT0, OUT1, OUT4			4	mA	
	Logic "HIGH", leakage current; external load to +V _{LOG} ; V _{OUT} = V _{LOG} max = 40V			0.1	0.2	mA	
Minimum pulse wid	lth		2			μs	
ESD protection	Human body r	nodel	±15			kV	
Digital Hall Inputs	(Hall1, Hall2, I	Hall3)	Min.	Тур.	Max.	Units	
Mode compliance		•	TTL		Open-coll	ector	
Default state	Input floating (wiring disconnected)		Logic HIGH				
	Logic "LOW"			0	8.0		
Input voltage	Logic "HIGH" Floating voltage		2	5 4.4		V	
	Absolute maxi	(not connected) Absolute maximum, surge			+15		
	(duration ≤ 1s) [†] Logic "LOW"; Pull to GND				1.2	ļ	
Input current	Logic "HIGH"; pull-up to +5	Internal 4.7KΩ	0	0	0	mA	
Minimum pulse wic			2			μs	
ESD protection Human body model		±5			kV		
Linear Hall Inputs (LH1, LH2, LH3)		Min.	Тур.	Max.	Units		
Input voltage	Operational ran	ge	0	0.5÷4.5	4.9	V	
la a control de la control	Absolute maximum values, continuous		-7		+7	V	
	Absolute maximum, surge (duration ≤ 1s) ^t		-11		+14		
Input current	Input voltage 0.	+5V	0		0.2	mA	
Interpolation Resolution	Depending on s settings	software			11	bits	
Frequency	Llower and Control	- 4-1	0		1	kHz	
ESD protection	Human body m	ouei	±1	<u> </u>]	kV	

1 @20kHz F _{PWI}	и	² The digital inputs are software selectable as PNP or NPN		
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Encoder1 Inputs						
(A1/A1+, A1-, B1/E	B1+, B1-, Z1/Z1+, Z1-)	Min.	Тур.	Max.	Units	
Single-ended mode compliance	Leave negative inputs disconnected	TTL /	CMOS /	Open-colle	ector	
Input voltage,	Logic "LOW"			1.6		
single-ended	Logic "HIGH"	1.8			V	
mode A/A+, B/B+	Floating voltage (not connected)		3.3			
Input voltage,	Logic "LOW"			1.2		
single-ended	Logic "HIGH"	1.4			V	
mode Z/Z+	Floating voltage (not connected)		4.7			
Input current,	Logic "LOW"; Pull to GND		5.5	6		
single-ended	Logic "HIGH"; Internal 2.2KΩ				mA	
mode A/A+, B/B+, Z/Z+	pull-up to +5	0	0	0		
Differential mode	For full RS422 compliance,		TIA /FIA	400.4	ı	
compliance	see 1		TIA/EIA	N-422-A		
Input voltage,	Hysteresis	±0.06	±0.1	±0.2	.,	
differential mode	Common-mode range (A+ to GND, etc.)	-7		+7	V	
Input impedance,	A1+ to A1-, B1+ to B1-		1			
differential	Z1+ to Z1-		1		kΩ	
	Single-ended mode, Open- collector / NPN	0		5	MHz	
Input frequency	Differential mode, or Single-					
	ended driven by push-pull	0		10	MHz	
	(TTL / CMOS)					
	Single-ended mode, Open- collector / NPN	1			μs	
Minimum pulse width	Differential mode, or Single-	+				
width	ended driven by push-pull	50			ns	
	(TTL / CMOS)					
Input voltage, any	Absolute maximum values, continuous	-7		+7		
pin to GND	Absolute maximum, surge				V	
	(duration ≤ 1s) t	-11		+14		
ESD protection	Human body model	±1			kV	
Encoder2 Inputs (A2+/Data+, A2-/D Z2-) ²	ata-, B2+/Clk+, B2-/Clk-, Z2+,	Min.	Тур.	Max.	Units	
Differential mode compliance	For full RS422 compliance, see ¹	TIA/EIA-422-A				
	Hysteresis	±0.06	±0.1	±0.2		
Input voltage	Differential mode	-14		+14	V	
put romago	Common-mode range (A+ to GND, etc.)	-11		+14	_	
Input impedance,	A2+, B2+, Z2+ A2-, B2-, Z2-		150		Ω	
Input frequency	Differential mode	0		10	MHz	
	Dillerential mode			10	IVITIZ	
Minimum pulse	Differential and I					
width	Differential mode	50			ns	
width Sin-Cos Encoder	Inputs		Тур.	Max.	ns Units	
width	Inputs Cos-)	50			Units	
width Sin-Cos Encoder (Sin+, Sin-, Cos+,	Inputs Cos-) Sin+ to Sin-, Cos+ to Cos-	50 Min.	1	1.25		
width Sin-Cos Encoder (Sin+, Sin-, Cos+, Input voltage,	Inputs Cos-) Sin+ to Sin-, Cos+ to Cos- Operational range	50			Units	
width Sin-Cos Encoder (Sin+, Sin-, Cos+, Input voltage,	Inputs Cos-) Sin+ to Sin-, Cos+ to Cos- Operational range Absolute maximum values,	50 Min.	1	1.25	Units V _{PP}	
width Sin-Cos Encoder (Sin+, Sin-, Cos+, Input voltage, differential	Inputs Cos-) Sin+ to Sin-, Cos+ to Cos- Operational range Absolute maximum values, continuous Absolute maximum, surge	50 Min. -1 -7	1	1.25 4 +7	Units	
width Sin-Cos Encoder (Sin+, Sin-, Cos+, Input voltage, differential Input voltage, any	Inputs Cos-) Sin+ to Sin-, Cos+ to Cos- Operational range Absolute maximum values, continuous Absolute maximum, surge (duration ≤ 1s) [†]	50 Min.	1	1.25	Units V _{PP}	
width Sin-Cos Encoder (Sin+, Sin-, Cos+, Input voltage, differential Input voltage, any pin to GND	Inputs Cos-) Sin+ to Sin-, Cos+ to Cos- Operational range Absolute maximum values, continuous Absolute maximum, surge (duration ≤ 1s) [†] Differential, Sin+ to Sin-,	50 Min. -1 -7	1	1.25 4 +7	Units V _{PP}	
width Sin-Cos Encoder (Sin+, Sin-, Cos+, Input voltage, differential Input voltage, any	Inputs Cos-) Sin+ to Sin-, Cos+ to Cos- Operational range Absolute maximum values, continuous Absolute maximum, surge (duration ≤ 1s) [†] Differential, Sin+ to Sin-, Cos+ to Cos- ³	50 Min. -1 -7 -11	1 2.5	1.25 4 +7	Units V _{PP} V	
width Sin-Cos Encoder (Sin+, Sin-, Cos+, Input voltage, differential Input voltage, any pin to GND Input impedance Resolution with	Inputs Cos-) Sin+ to Sin-, Cos+ to Cos- Operational range Absolute maximum values, continuous Absolute maximum, surge (duration ≤ 1s) † Differential, Sin+ to Sin-, Cos+ to Cos- 3 Common-mode, to GND Software selectable, for one	50 Min. -1 -7 -11 4.2	1 2.5 4.7	1.25 4 +7 +14	$\begin{array}{c} \text{Units} \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\$	
width Sin-Cos Encoder (Sin+, Sin-, Cos+, Input voltage, differential Input voltage, any pin to GND Input impedance	Inputs Cos-) Sin+ to Sin-, Cos+ to Cos- Operational range Absolute maximum values, continuous Absolute maximum, surge (duration ≤ 1s) [†] Differential, Sin+ to Sin-, Cos+ to Cos- ³ Common-mode, to GND Software selectable, for one sine/cosine period	50 Min. -1 -7 -11 4.2	1 2.5 4.7	1.25 4 +7 +14	$\begin{array}{c} \text{Units} \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\$	
width Sin-Cos Encoder (Sin+, Sin-, Cos+, Input voltage, differential Input voltage, any pin to GND Input impedance Resolution with	Inputs Cos-) Sin+ to Sin-, Cos+ to Cos- Operational range Absolute maximum values, continuous Absolute maximum, surge (duration ≤ 1s) † Differential, Sin+ to Sin-, Cos+ to Cos- 3 Common-mode, to GND Software selectable, for one	50 Min. -1 -7 -11 4.2	1 2.5 4.7	1.25 4 +7 +14	$\begin{array}{c} \text{Units} \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\$	

Analog 05V Inp	outs (REF, FDBK)	Min.	Тур.	Max.	Units
	Operational range	0	,,	5	
	Absolute maximum values,	-12		+18	
Input voltage	continuous	-12		. 10	V
	Absolute maximum, surge			±36	
land the same days as	(duration ≤ 1s) [†]		20		1.0
Input impedance Resolution	10 GND		28 12		kΩ bits
Integral linearity			12	±2	bits
Offset error			±2	±10	bits
Gain error			±1%	±3%	% FS⁴
Bandwidth (-3Db)	Software selectable	0		1	kHz
ESD protection	Human body model	±5			kV
RS-232		Min.	Тур.	Max.	Units
Compliance			TIA/EIA	\-232-C	
Bit rate	Software selectable	9600	0	115200 anteed	Baud
Short-circuit ESD protection	232TX short to GND Human body model	±2	Guara	anteed	kV
Safe torque OFF	Human body model				
(STO1+,STO1-; S	TO2+, STO2+)	Min.	Тур.	Max.	Units
Safety function	According to EN61800-5-2	S	TO (Safe T	Forque OF	F)
EN 61800-5-1/ -2	Safety Integrity Level	safe	ty integrity	/ level 3 (SIL3)
and EN 61508-5-	PFHD (probability of dangerous				
3/ -4 Classification	failures per hour)	8*10 ⁻¹⁰	hou	ur-1 (0.8 F	T)
Classification	, ,		Cata	/DL a	
EN13849-1	Performance Level		Cats	/PLe	1
Classification	MTTFM (meantime to dangerous failure)		377		years
Mode	dangerous failure)				l .
compliance			PI	NΡ	
•	Input floating (wiring		Lasia	1.0\\\	
Default state	disconnected)		Logic	LOW	
	Logic "LOW"	-20		5.6	
Input voltage	Logic "HIGH"	18		36	V
	Absolute maximum, continuous	-20	•	+40	
Input current	Logic "LOW"; pulled to GND Logic "HIGH", pulled to +Vlog		<u>0</u> 5	13	mA
			5		
Repetitive test	Ignored high-low-high			5	ms
pulses				20	Hz
(high-low-high)				20	
Fault reaction	From internal fault detection to				
time	register DER bit 14 =1 and			30	ms
	OUT2/Error high-to-low				
DIA/AA	From external STO low-high				
PWM operation delay	transition to PWM operation			30	ms
delay	enabled				
ESD protection	Human body model	±2			kV
Ethernet Ports		Min.	Тур.	Max.	Units
	EtherCAT (IEC	C61158-3	3/4/5/6-12)		
Standard	Fast Ethernet 100E				
Compliance	Auto-negotiation fo			ıplex	
	Auto-dete	ect MDI/N	/IDI-X		
Power over	NOT used by the		liant to IEE		
Ethernet	iPOS4808MY, requires		A "Mixed E		
	separate +Vlog SELV/ PELV supply		compliant ode B "DC		
Isolation	Requirement for motherboard	500	OUE D DC	on opan	V _{rms}
GND0,GND1	PCB routing	1.5		 	kV _{peak}
Maximum cable	2-pair UTP Cat5	100	150		m
length	•				
ESD protection	Human body model	±4			kV
LED signals		Min.	Тур.	Max.	Units
LED connection		Co	mmon cat	hode to G	ND
55.1110011011					
LED	T	L D	rect, no se		
LED current 3.3 output	 	2 1 5	8	10	mA V
voltage		3.15	3.3	3.45	٧
3.3 output		1		60	mA
current					
	nd values listed under "absolute m				

[†] Stresses beyond values listed under "absolute maximum ratings" may cause permanent damage to the device. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

ected across 4 "FS" stands for "Full Scale"

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 $^{^1}$ For full RS-422 compliance, 120Ω termination resistors must be connected across the differential pairs, as close as possible to the drive input pins.

 $^{^2}$ Encoder2 differential input pins have internal $120\Omega\,\text{termination}$ resistors connected across

 $^{^3}$ For many applications, a 120 Ω termination resistor should be connected across SIN+ to SIN-, and across COS+ to COS-. Please consult the feedback device datasheet for confirmation.

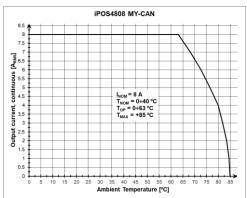


Figure 1. iPOS4808 MY-CAN De-rating with ambient temperature¹

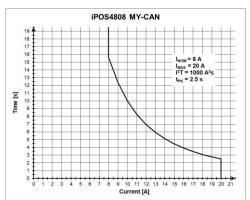


Figure 2. iPOS4808 MY-CAN Over-current diagram¹⁰

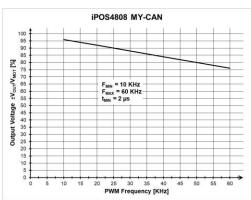


Figure 3. iPOS4808 MY-CAN Output Voltage De-rating with PWM frequency¹⁰

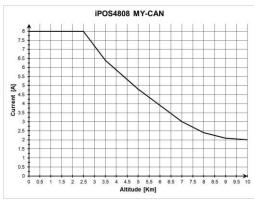


Figure 4. iPOS4808 MY-CAN De-rating with altitude¹⁰

 ${1\over 1} \ \ {\rm Measured\ under\ the\ following\ conditions:\ BLDC;\ Vmot=48V,\ Vlog=24V,\ PWM=20kHZ}$

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